# Lab Manual 12

# Sequence Recognizer

## Objectives:

To learn how to design a sequence recognizer

**JK Flip-Flop**

This device LS76A contains two independent negative-edge-triggered JK-type flip-flops with complementary outputs. The information on the J and K is accepted by the flip-flops on the negative going edge of the clock pulse. LOW logic level on the preset or clear inputs will set or reset the outputs regardless of the logic levels of the other inputs.

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| Image result for SN7476 |  |

## Problems / Assignments

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| Problem 1 |  |

Design & implement 1101 sequence recognizer using JK Flip-Flop on Logic works